KillTest

更に上のクオリティ　更に上のサービス

問題集

http://www.killtest.jp

1年で無料進級することに提供する

Exam : **HP0-P13**

Title : Planning & Design of HP Integrity Server Solutions

Version : Demo

1. Which functions are provided by Integrity Virtual Machines but not by hard partitions?

A. failover protection for high availability

B. ability to run multiple operating systems simultaneously

C. electrical isolation

D. dynamic resource allocation

Answer: D

2. When you are designing an HP Integrity solution, which HP tool should you use to determine power consumption?

A. Capacity Advisor

B. sizing tool

C. Performance Quick Reference Tool (PQRT)

D. power calculator

Answer: D

3. Your customer wants to permanently move processor usage rights within a server. Which type of licensing is needed?

A. TiCAP

B. vPar

C. GiCAP

D. iCAP

Answer: D

4. Which feature of an HP Integrity cell-based server enables you to replace a failed processor on one partition, while the other partition is running?

A. dynamic processor deallocation

B. Instant Capacity (iCAP)

C. electrically isolated partitions

D. hot plug processors

Answer: C

5. Which feature of the HP sx2000 chipset makes the Integrity rx7640 server a better choice for high availability than the rx6600?

A. double chip spare

B. Hyper-Threading

C. link self-healing

D. Dynamic Processor Resilience

Answer: C

6. A customer is currently using 32-bit processors in their data center. At present, they are not running out of disk space for their applications, and they have not had to create partitions for their databases.

What advantage can this customer gain by switching to 64-bit computing? (Select two.)

A. greater security and availability

B. reduced diskspace costs

C. faster response times for queries

D. fewer processors required for tasks

E. potential reduction in licensing costs

Answer: DE

7. Which applications will benefit most from porting from 32-bit to Itanium 64-bit processors? (Select two.)

A. web services

B. floating point calculations

C. very large databases

D. online transaction processing

E. mission-critical applications

Answer: BC

8. Which high-availability feature keeps a system running in spite of a multi-bit DRAM error?

A. chip spare like memory

B. memory scrubbing

C. dynamic page deallocation

D. address control parity

Answer: A

9. Which statements are true about memory in cell-based Integrity systems? (Select two.)

A. Memory modules of different sizes can be mixed in the same cell.

B. Cell local memory can be configured as a percentage of memory.

C. Cell local memory is faster than interleaved memory for providing access to memory on other cells in the system.

D. Memory must be installed in dual quads of the same size memory modules.

E. Cell-based systems require cell local memory.

Answer: AB

10. Which statement is true regarding I/O technologies in HP Integrity servers?

A. PCIe uses a dual simplex point-to-point topology to move data.

B. PCI-X and PCIe support complex routing.

C. PCI, PCI-X, and PCIe use parallel buses.

D. PCI and PCIe are connector-compatible.

Answer: A