

KillTest

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問題集

<http://www.killtest.jp>

1年で無料進級することに提供する

Exam : **E20-617**

Title : **DMX Migration**

Version : **DEMO**

1. How is vault integrity verified?

- A. Customer data management software
- B. Run inlines manually once a day as a PM
- C. Checks are performed every 500 millisecond thru a script
- D. EMC ControlCenter does it automatically and informs the customer if the vault is ok

Answer: C

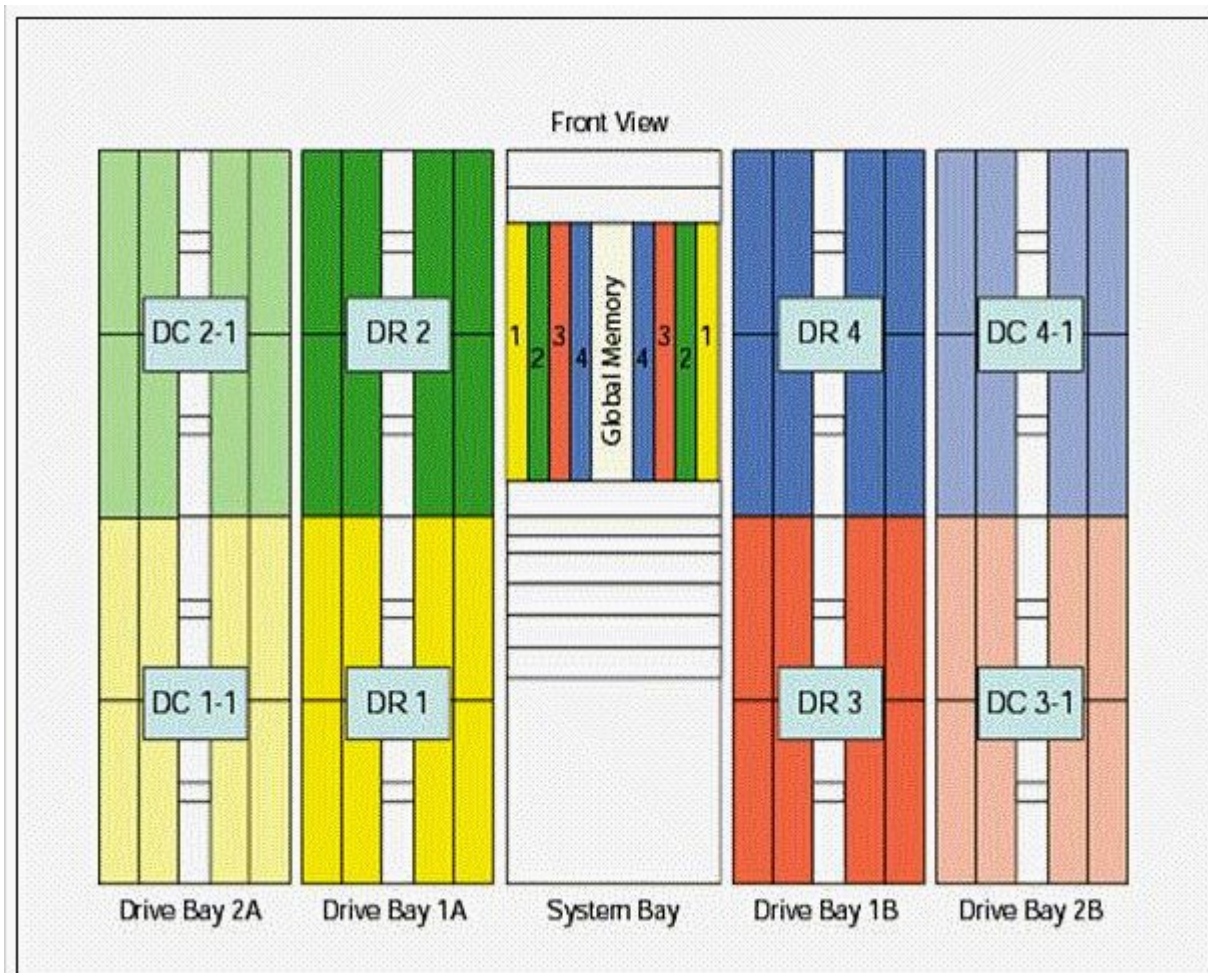
2. Where are volume flags for SRDF devices set?

- A. Disk Map
- B. Director Edit
- C. Volume Edit
- D. Volume Request

Answer: C

3. Click the Exhibit button.

Which directors are connected to DR-1?



- A. Directors 1 and 16
- B. Directors 2 and 15

- C. Directors 5 and 12
- D. Directors 6 and 11

Answer: A

4. Which bit is set on the Edit Directors page for FA link speed Auto Negotiate?

- A. ARB
- B. EAN
- C. VCM
- D. SC3

Answer: B

5. What is the maximum number of drives that can be configured on a DMX3 direct connect drive loop?

- A. 4
- B. 9
- C. 15
- D. 18

Answer: C

6. What changes during a block move in SymmWin 5670+?

- A. Attributes
- B. Addresses
- C. Symmetrix volume numbers
- D. Physical location on the disk

Answer: C

7. A DMX3 ships with 960 physical drives. How many drive bays are needed?

- A. 2
- B. 4
- C. 8
- D. 16

Answer: B

8. After adding bay 2, what needs to be done for the Symmetrix DMX3 to recognize the drives?

- A. Set LCC to reflect proper bay
- B. Upgrade LCC card to the new version
- C. Configure thru the impl init screen in symmwin
- D. Nothing; the LCC card will discover new drives automatically

Answer: A

9. Where are the larger size cache boards in the memory board slots of a DMX3 located?

- A. Larger size cards go on the left
- B. Larger sizes always go to the right
- C. Larger size cards go in the middle
- D. There is no restriction on where the boards are located

Answer: B

10. How many copies of global memory reside on the vault drives?

- A. 1
- B. 2
- C. 4
- D. 6

Answer: B